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## \*\* CONTINUING DATA \*\*\*\*\*

This appln claims benefit of 60/501,131 09/08/2003

## \*\* FOREIGN APPLICATIONS \*\*\*\*\*

## IF REQUIRED, FOREIGN FILING LICENSE GRANTED

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Foreign Priority claimed	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no	STATE OR COUNTRY INDIA	SHEETS DRAWING 5	TOTAL CLAIMS 8	INDEPENDENT CLAIMS 4
35 USC 119 (a-d) conditions met	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance				
Verified and Acknowledged		Initials			

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## TITLE

At-speed ATPG testing and apparatus for SoC designs having multiple clock domain using a VLCT test platform

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